

SHEET INDEX		
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# SYMBOL

INPUT LEVEL SELECT  
GROUP IDENT.

TERM.	WBD	FUNC	TERM.	WBD	FUNC	TERM.	WBD
G1	I	007	282	P1	0	200	289
G2	I	111	282	P1	0	305	289
G3	I	107	282	P2	0	225	289
G4	I	010	282	P2	0	304	289
G5	I	106	282	P3	0	204	289
G6	I	012	282	P4	0	305	289
G7	I	105	282	P4	0	235	289
G8	I	014	282	P4	0	302	289
G9	I	004	282	P5	0	202	289
G10	I	115	282	P5	0	301	289
G11	I	003	282	P6	0	201	289
G12	I	116	282	P6	0	300	289
G13	I	100	282	P7	0	207	289
G14	I	117	282	P7	0	306	289
G15	I	100	282	P8	0	209	289
G16	I	019	282	P8	0	308	289
G17	I	101	282	P9	0	200	289
G18	I	018	282	P10	0	212	289
G19	I	001	282	P10	0	311	289
G20	I	118	282	P11	0	213	289
G21	I	002	282	P11	0	312	289
G22	I	017	282	P12	0	214	289
G23	I	103	282	P12	0	313	289
G24	I	016	282	P13	0	215	289
G25	I	104	282	P13	0	314	289
G26	I	015	282	P14	0	219	289
G27	I	005	282	P15	0	218	289
G28	I	114	282	P15	0	315	289
G29	I	008	282	P16	0	217	289
G30	I	113	282	P16	0	316	289
G31	I	109	282	P17	0	216	289
G32	I	013	282	P17	0	315	289
G33	I	009	282				
G34	I	112	282				
G35	I	211	282				
G36	I	210	282				
G37	I	310	282				

# SYMBOL

007	L1	P1	206
111	L2	P1	305
107	L3	P2	205
010	L4	P2	304
106	L5	P3	204
012	L6	P3	303
105	L7	P4	203
014	L8	P4	302
004	L9	P5	202
115	L10	P5	301
003	L11	P6	201
116	L12	P6	300
100	L13	P7	207
117	L14	P7	306
100	L15	P8	209
019	L16	P8	308
101	L17	P9	208
018	L18	P9	307
001	L19	P10	212
118	L20	P10	311
002	L21	P11	213
103	L22	P11	312
016	L23	P12	214
104	L24	P12	313
005	L25	P13	215
114	L26	P13	314
008	L27	P14	219
113	L28	P14	318
009	L29	P15	218
112	L30	P15	317
011	L31	P16	217
102	L32	P16	316
019	L33	P17	216
111	L34	P17	315
210	L35		
310	L36		

FC192  
INPUT LEVEL  
SELECT

RECORD OF CHANGES				
DATE	PREV	WFS	SEC	NOTE
ISS	FURN	STD	01SC	

# CIRCUIT DESCRIPTION

## FUNCTION

THIS CIRCUIT PROVIDES FOR CONTROLLING ONE OUT OF SEVENTEEN HIGH CURRENT PULSE PATH FROM RELATIVELY LOW POWER SIGNALS. THIS FUNCTION IS PROVIDED FOR DUPLICATED CONTROLLERS.

## DETAILED DESCRIPTION

THE HIGH AMPLITUDE CURRENT PULSE FLOWS INTO ONE OF THE P1-P17 LEADS AND OUT THE P8 LEAD. A PARTICULAR PATH IS SELECTED BY APPLYING 15 VOLTS ON ONE OF THE G1-G34 LEADS AND GROUNDING LEAD N1.

# NOTES:

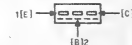
- UNLESS OTHERWISE SPECIFIED:  
RESISTANCE VALUES ARE IN OHMS  
CAPACITANCE VALUES ARE IN MICROFARADS  
VALUES PRECEDED BY THE SYMBOL " (+) " OR " (-) " ARE IN VOLTS.
- POWER AND GROUND TERMINALS FOR INTEGRATED CIRCUITS:

IC CODE	GND TEMP			

- BATTERY AND GROUND TERMINALS FOR THIS CIRCUIT PACK ARE AS FOLLOWS:

FUNCTION	TERMINAL
+V	(20)
GND	(200, 219)

- THE TERMINAL NUMBER ARRANGEMENT OF THE BSA TRANSISTOR IS:



- CLOSEST HORIZONTAL MOUNTING CENTERS IS 0.750 INCHES.
- INITIAL ISSUE OF THE FC192 CIRCUIT PACK IS IN 50-34110-01.

SYSTEM USED ON	DESIGN CONTROL
NO. 3 ESS	IN

CURRENT DRAWING: 0 mA

# SHEET INDEX NOTES

- FOR SINGLE REISSUES, A CHANGED OR NEW SHEET WILL BE ASSIGNED THE SAME ISSUE NUMBER AS SHEET 1.
- FOR CONCURRENT REISSUES, A CHANGED OR NEW SHEET WILL BE ASSIGNED THE HIGHEST ISSUE NUMBER ATTESTING THAT SHEET.
- THE ISSUE NUMBER OF SHEET 1 IS RECOGNIZED AS THE ISSUE NUMBER OF THE WHOLE DRAWING.

# SUPPORTING INFORMATION

CATEGORY	NUMBER
CONNECTOR ON FRAME	9478, 947C (ON 947E)
CIRCUIT PACK INFORMATION DRAWING	
SERIES FOR LATEST CLASS "A" CHANGE	
ACCEPTABLE SERIES	4

NOTICE - NOT FOR USE OR DISCLOSURE OUTSIDE THE BELL SYSTEM EXCEPT UNDER WRITTEN AGREEMENT.

FC192 CIRCUIT PACK  
INPUT LEVEL SELECT  
CIRCUIT

BELL LABORATORIES

CPS - FC192

6S

ISSUE

201

3 SHEETS

## COMPONENT LIST

## CAPACITOR

DESIG CODE  
[17]C1-C17 K5 20756 .1. .1

## DIODE

DESIG CODE  
[34]CR1-CR34 4968  
[17]CR35-CR51 9338

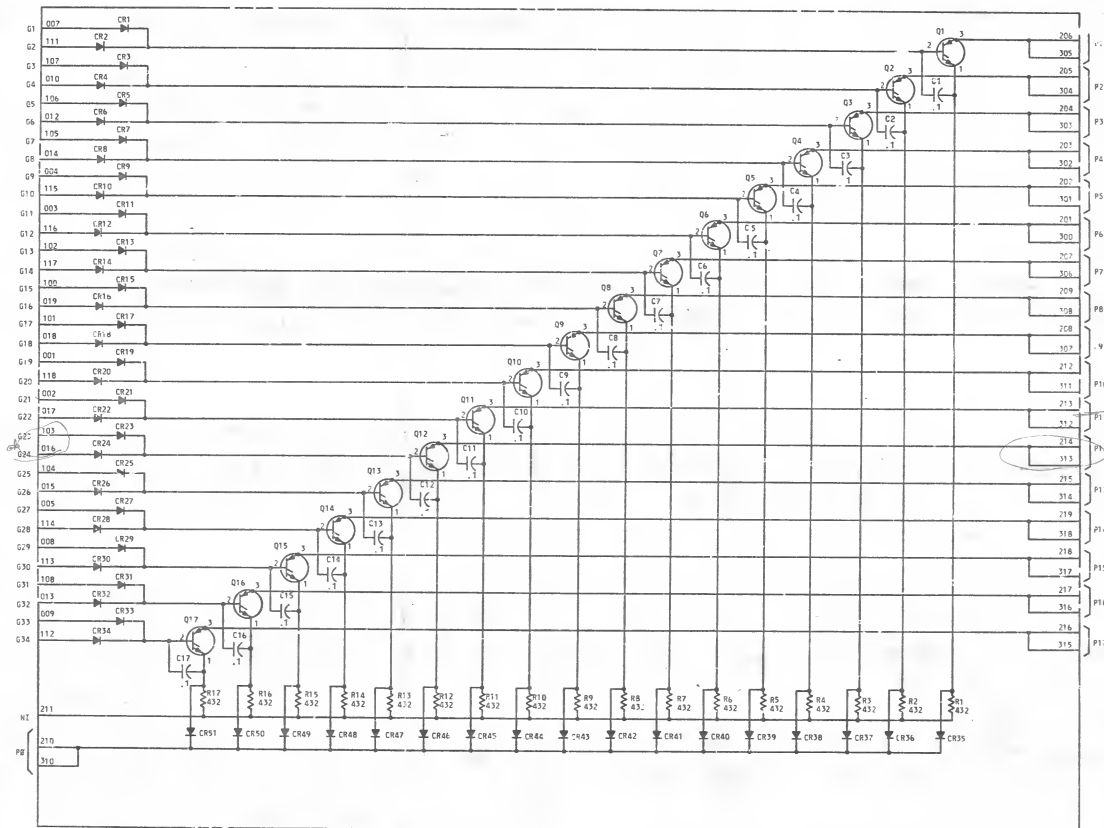
## RESISTOR

DESIG CODE  
[17]R1-R17 K5-208 0.11A. 432

## TRANSISTOR

DESIG CODE  
[17]Q1-Q17 85A

## INPUT LEVEL SELECT CIRCUIT



201

FC192 CIRCUIT PACK

BELL TELEPHONE LABORATORIES  
INCORPORATED

CPS-FC192  
Sheet 2

6S